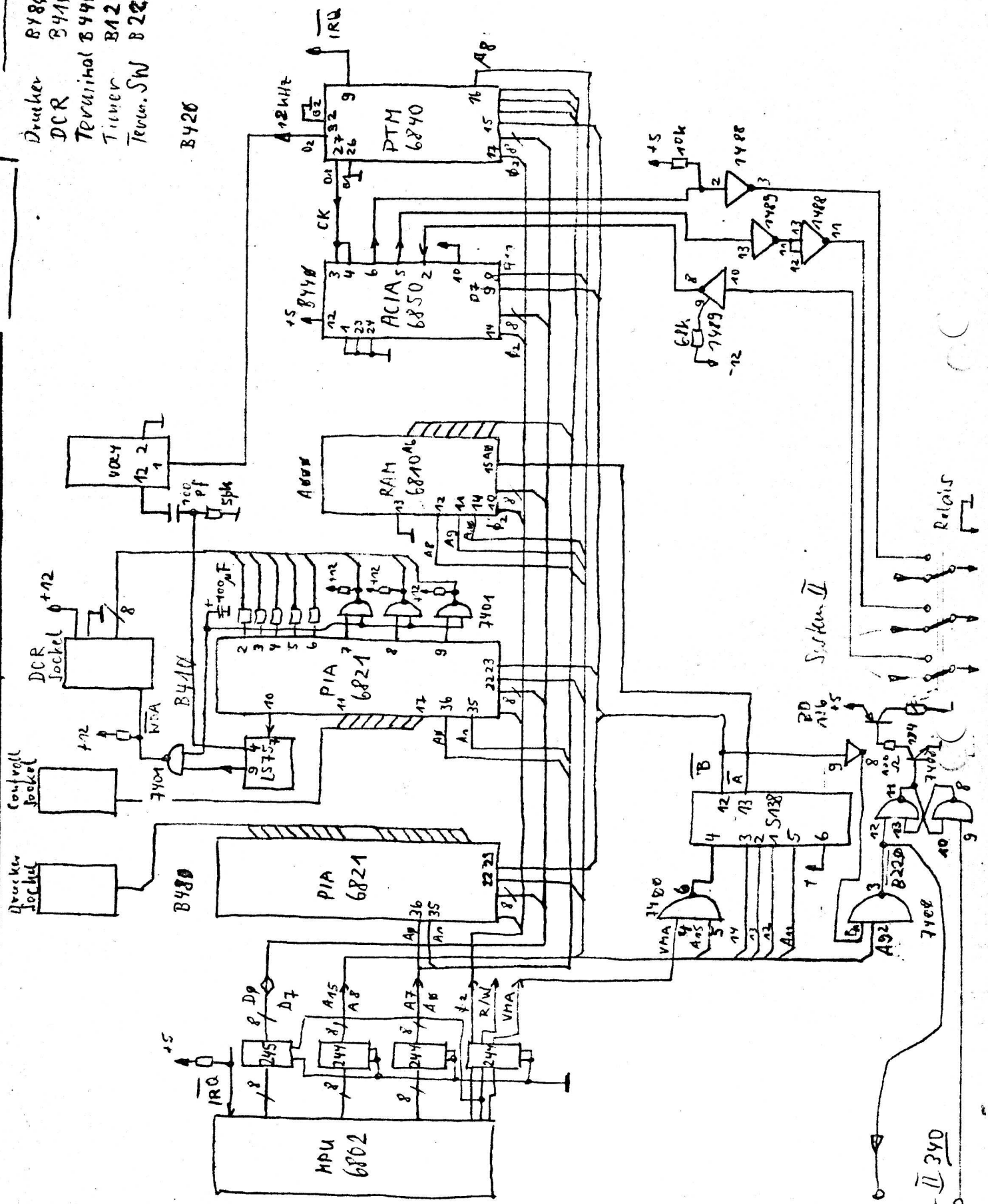


Platine Peripherie - 340 Systems - 380-Event

Adresse	Bitmuster
Drucker B480-3	A7 6 5 4
DCR B410-3	1 0 0 0 1
Terminal B440-1	0 0 0 1
Timer B420-7	0 0 0 0
Termin. SW B220	0 0 0 0

ACIR - 6111
300 k114

B420



2401
LTC
B220
4 System II 340
3820

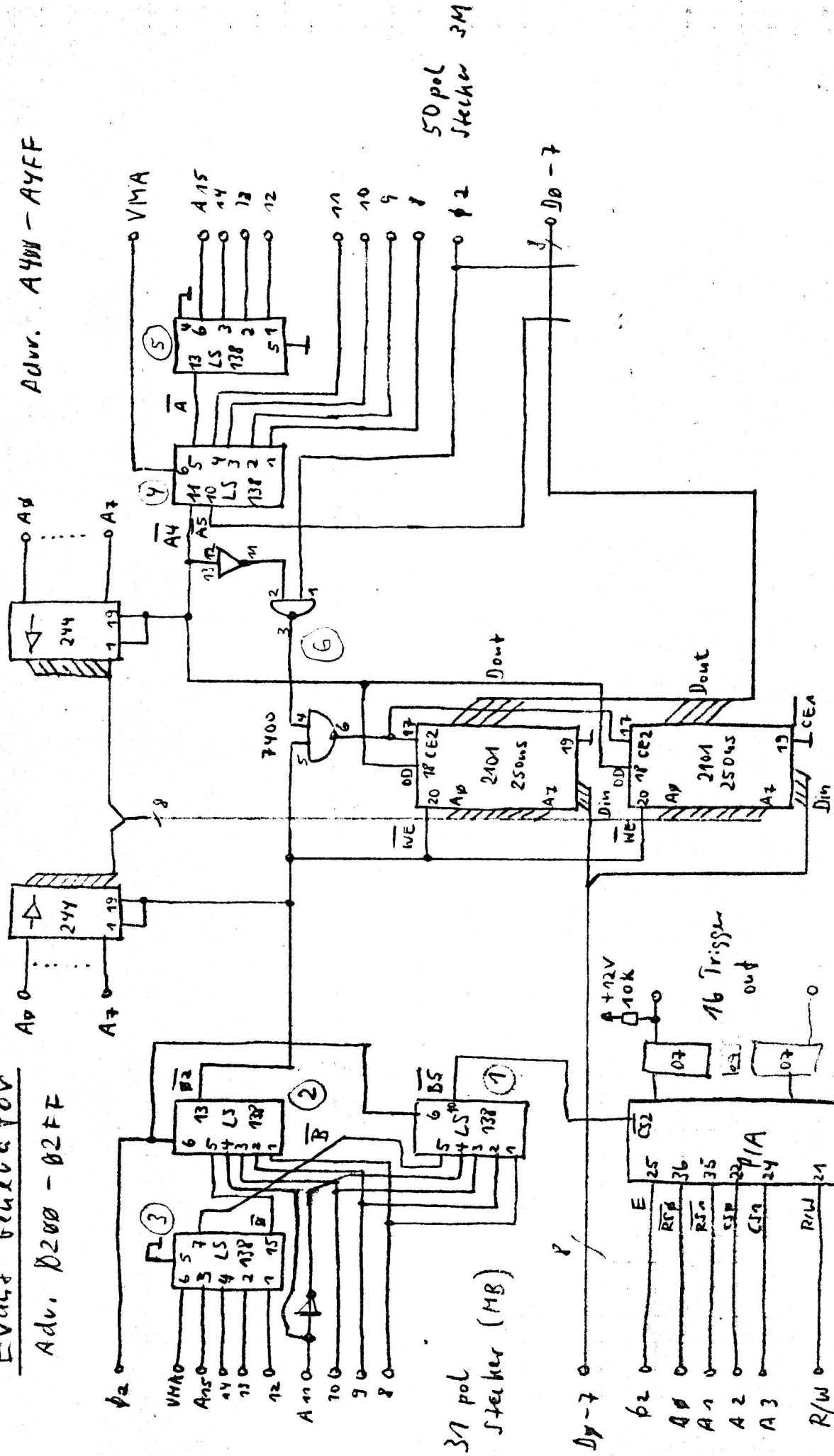
Datenbuffer - RAM

Event Generator

Adv. D200 - 02FF

340 Wave Comp.

Addr. A400 - A4FF

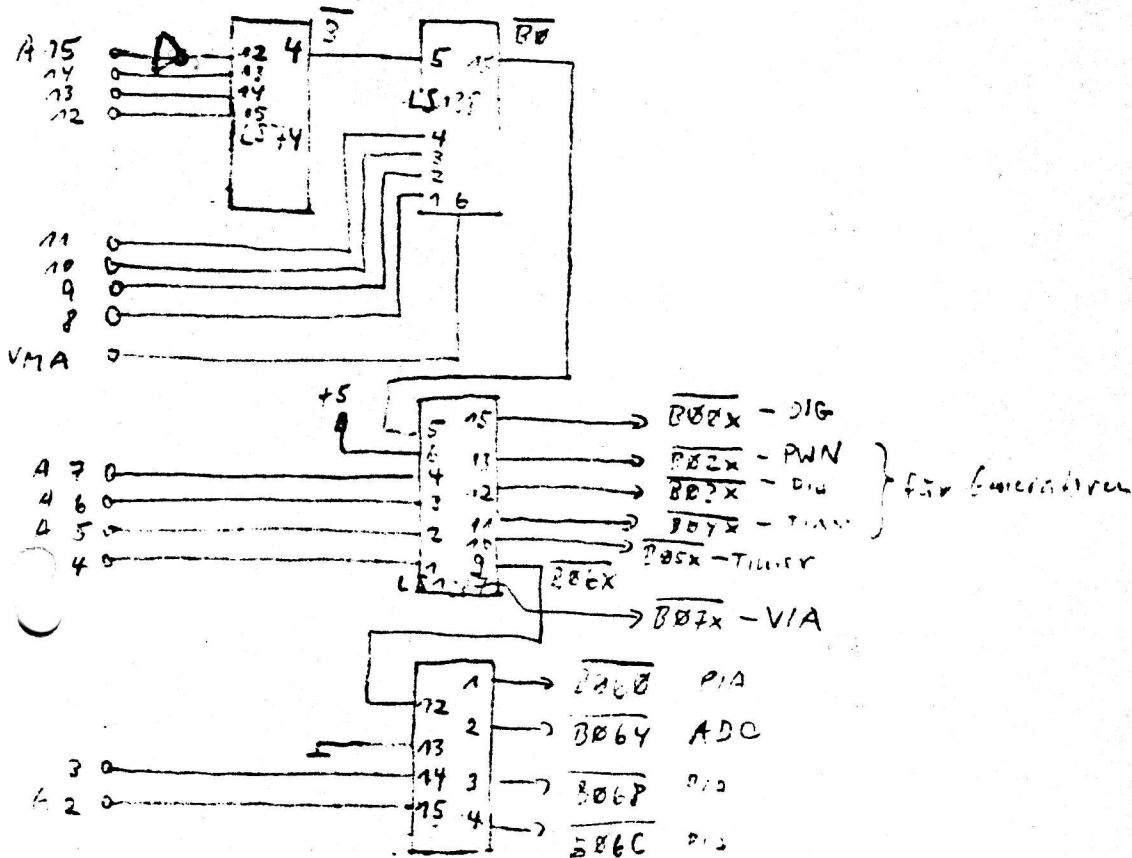


Adresse BDXC - BDXF

Datenflus

Adressdecodierung auf Platine 1-0 360 B

340 Generator Unit

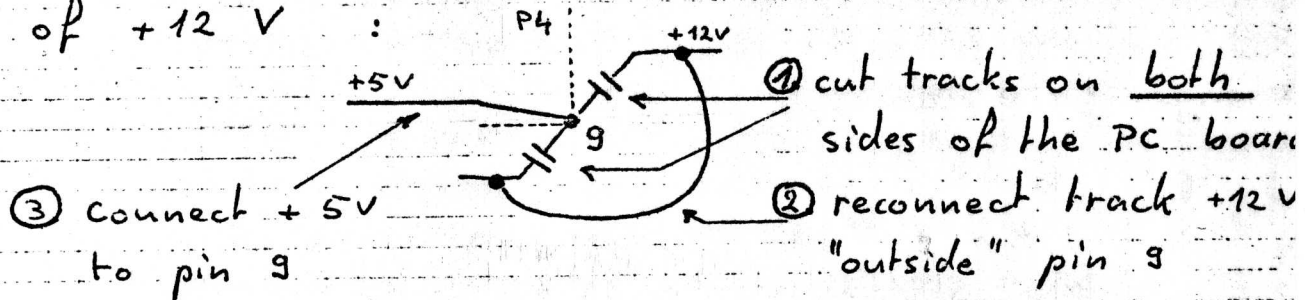


CASSETTE INTERFACE M68C11-1
USED WITH OLD VERSIONS OF PDS MOTHER BOARD
("CATIKIT" AND "MOTHER BOARD REV 2")

1. MODIFICATIONS ON PDS MOTHER BOARD

"CATIKIT" VERSION:

Connect +5V to pin 9 / connector P4 instead of +12V :

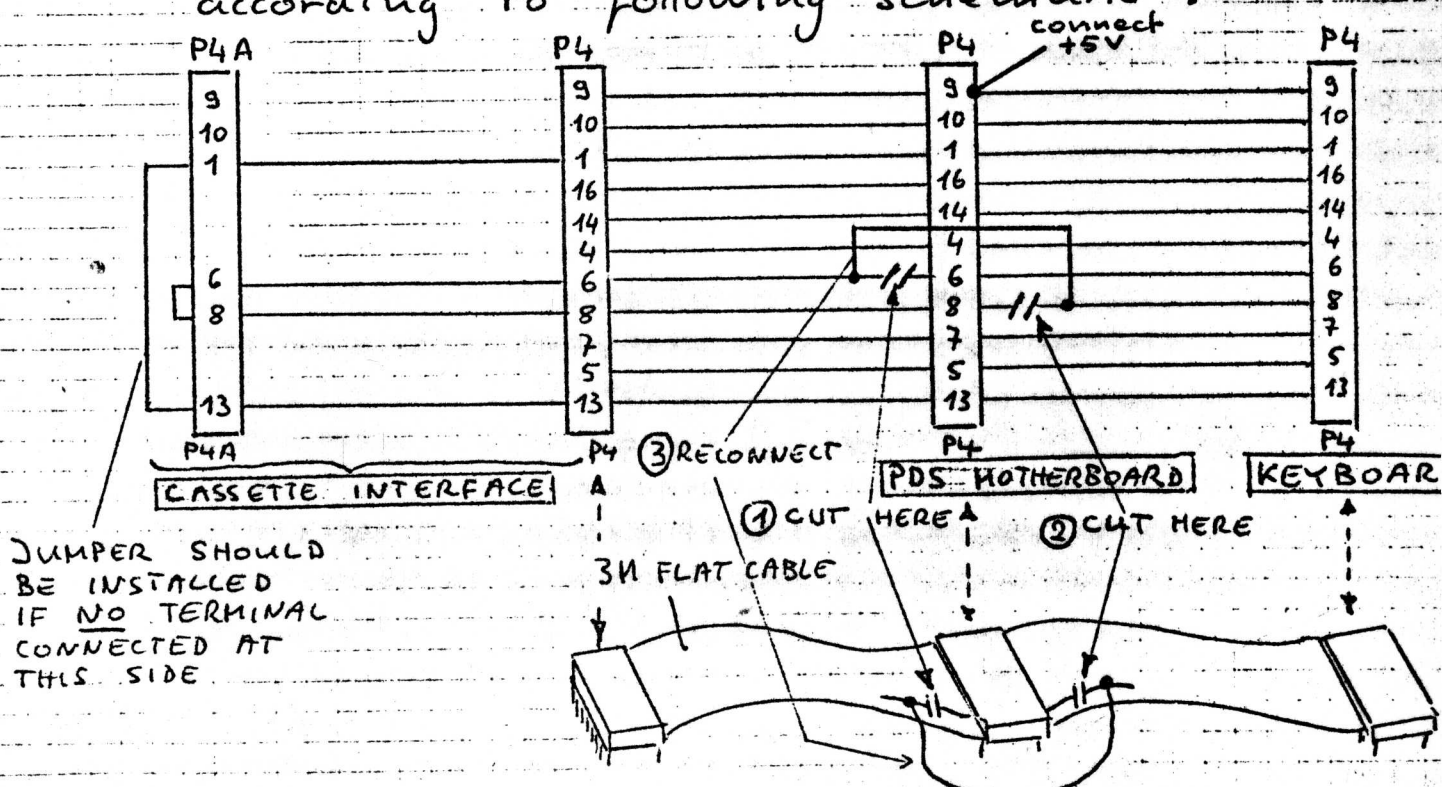


"MOTHER BOARD REV 2" :

Connect +5V to pin 9 / connector P4

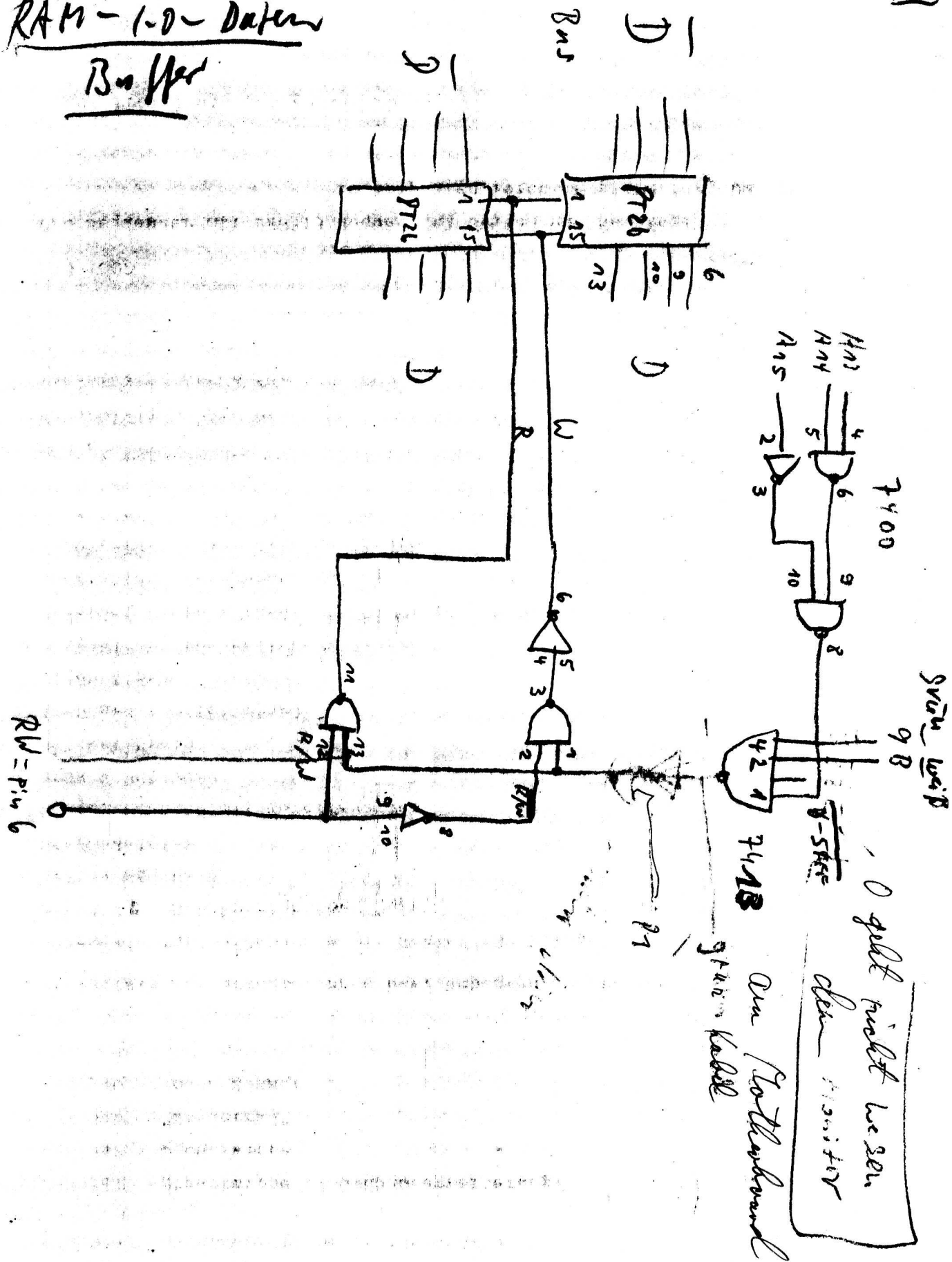
2. INTERCONNECT CABLE

Modify or built the interconnect cable according to following schematic :



E

RAM - 1.0 - Daten
Buffer



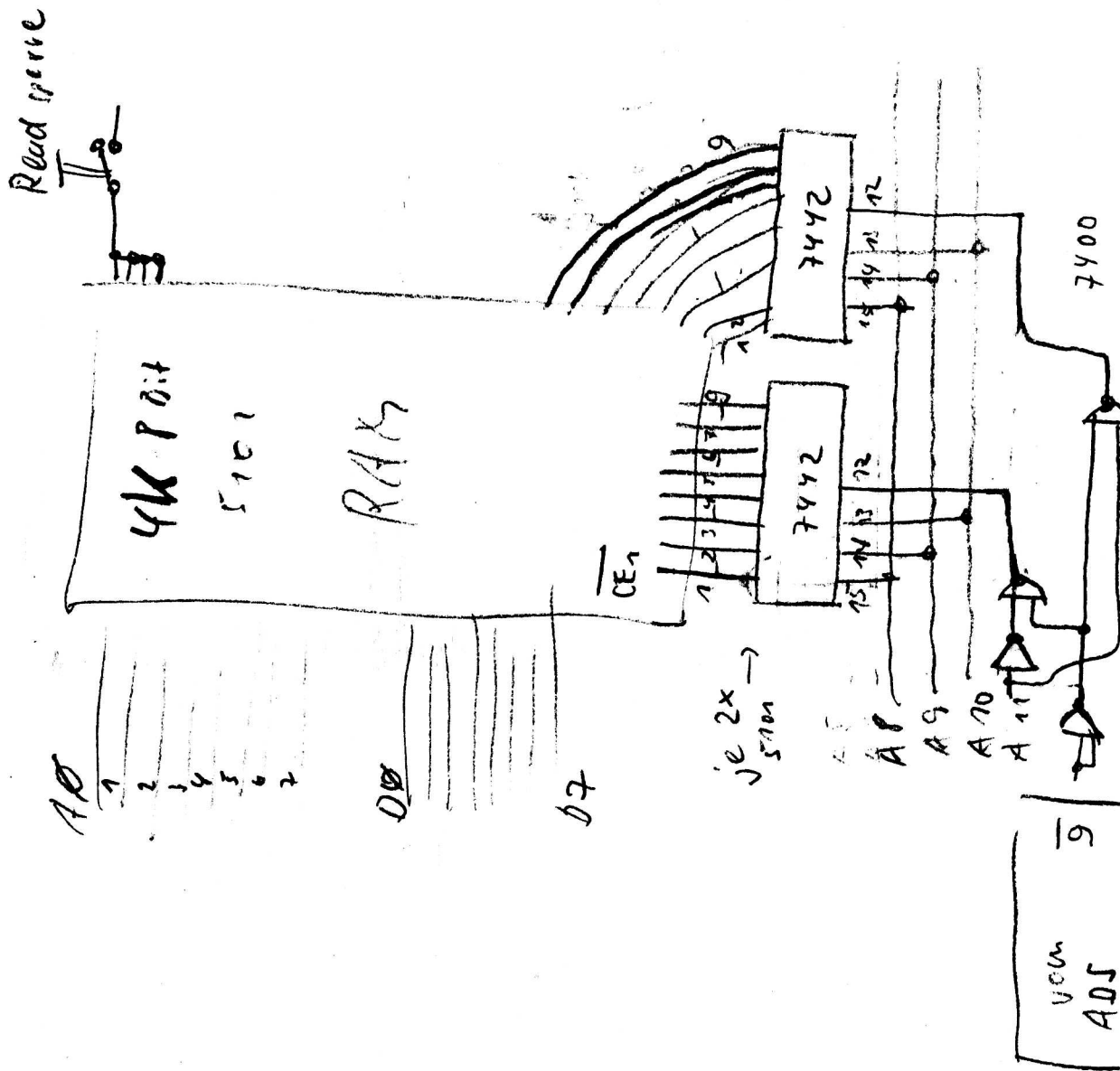
Address

Extré platina

Zeit 500 Tückste

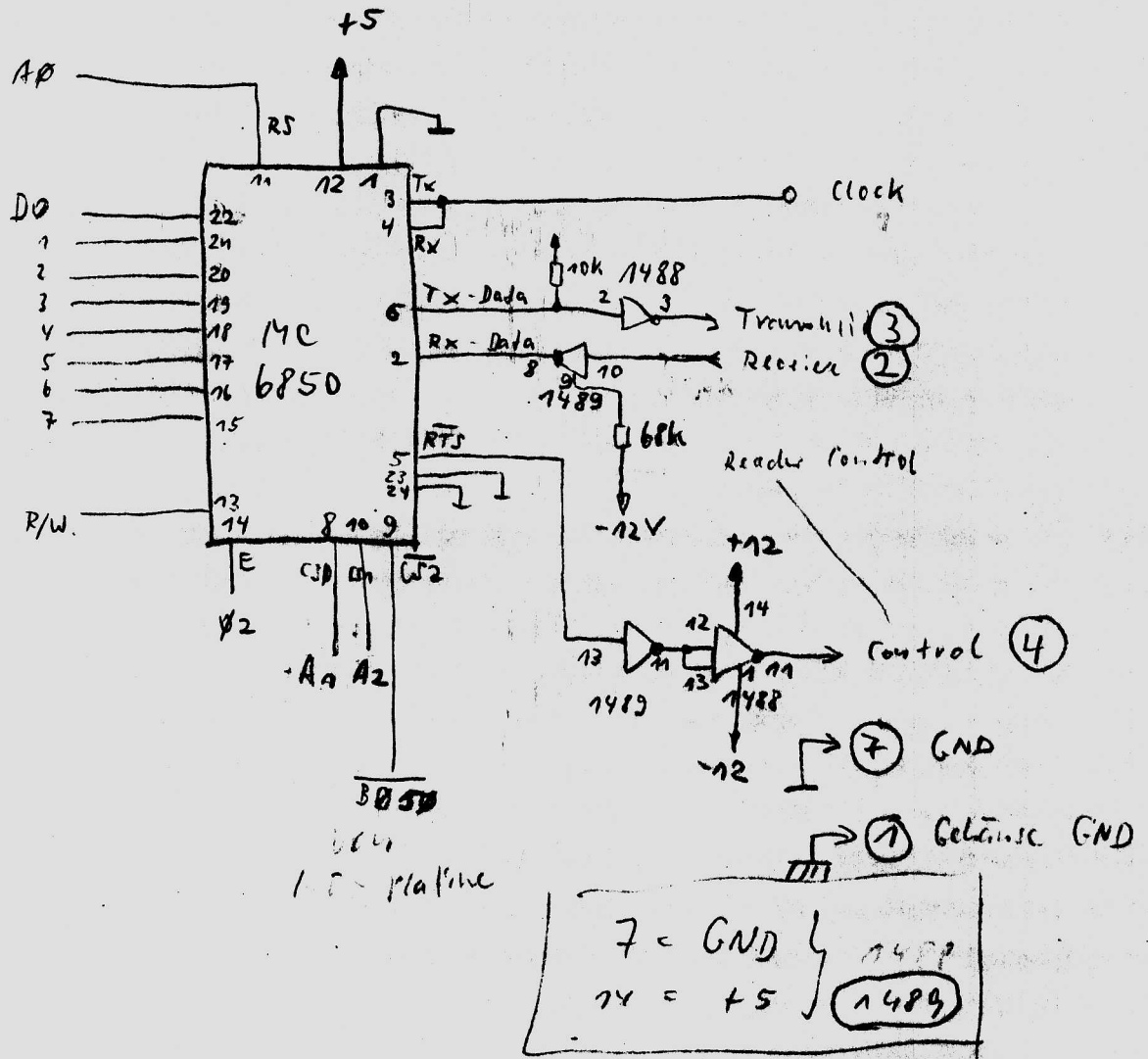
986-987F

Time = 9000 ans ⁵



Levielle Schnittstelle

B056
B057

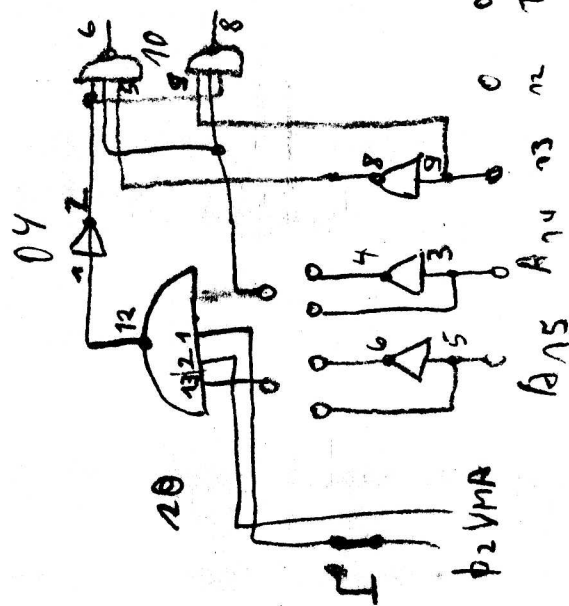


Stecker 31 pol.

Pin 26 - B050

" 28 - -12V

16 K RAM



16 K

RAM

16 K

RAM

16 K

RAM

16 K

RAM

16 K

RAM

16 K

RAM

16 K

RAM

16 K

RAM

16 K

RAM

16 K

RAM

16 K

RAM

16 K

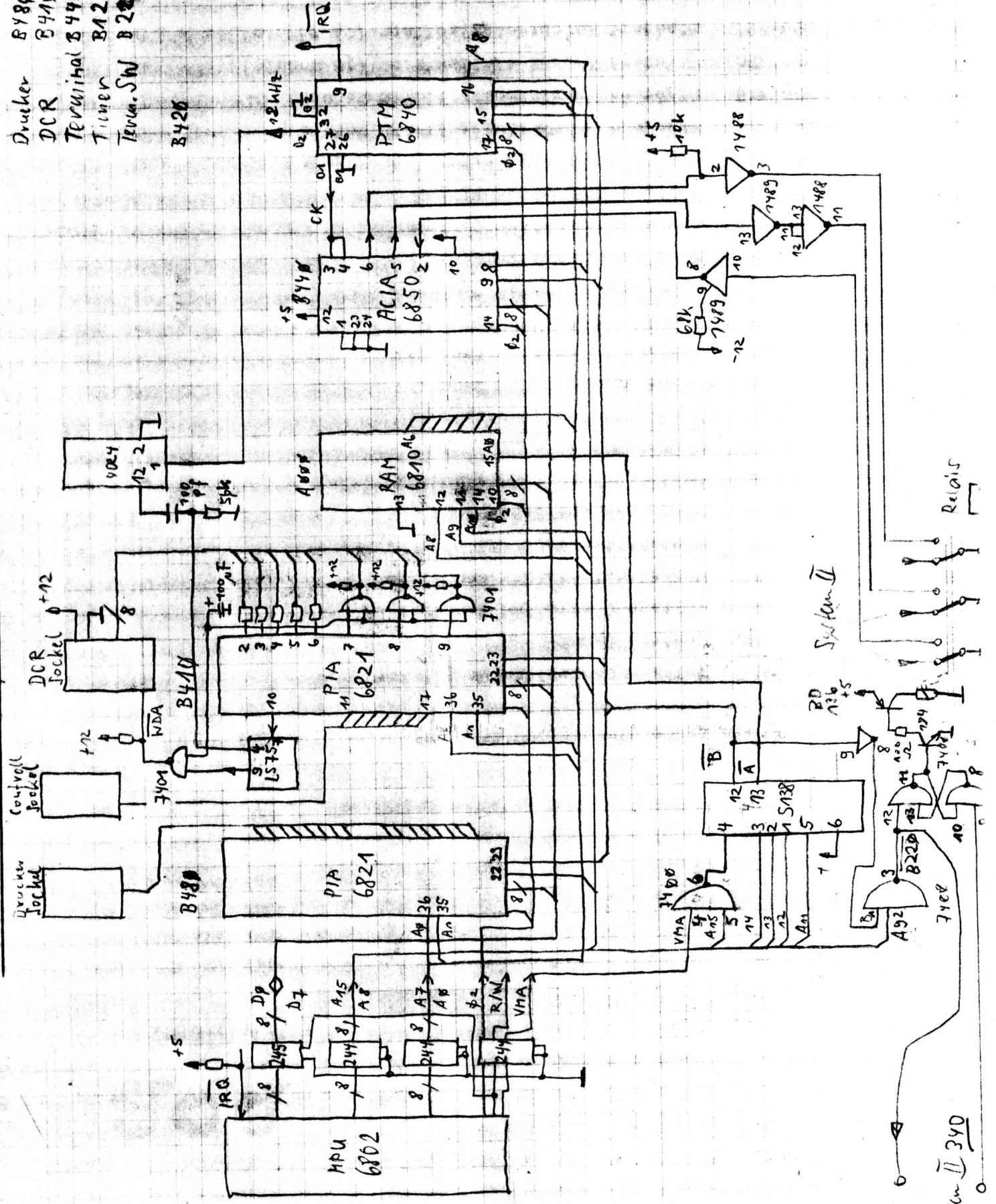
RAM

16 K

RAM

September 3. H.

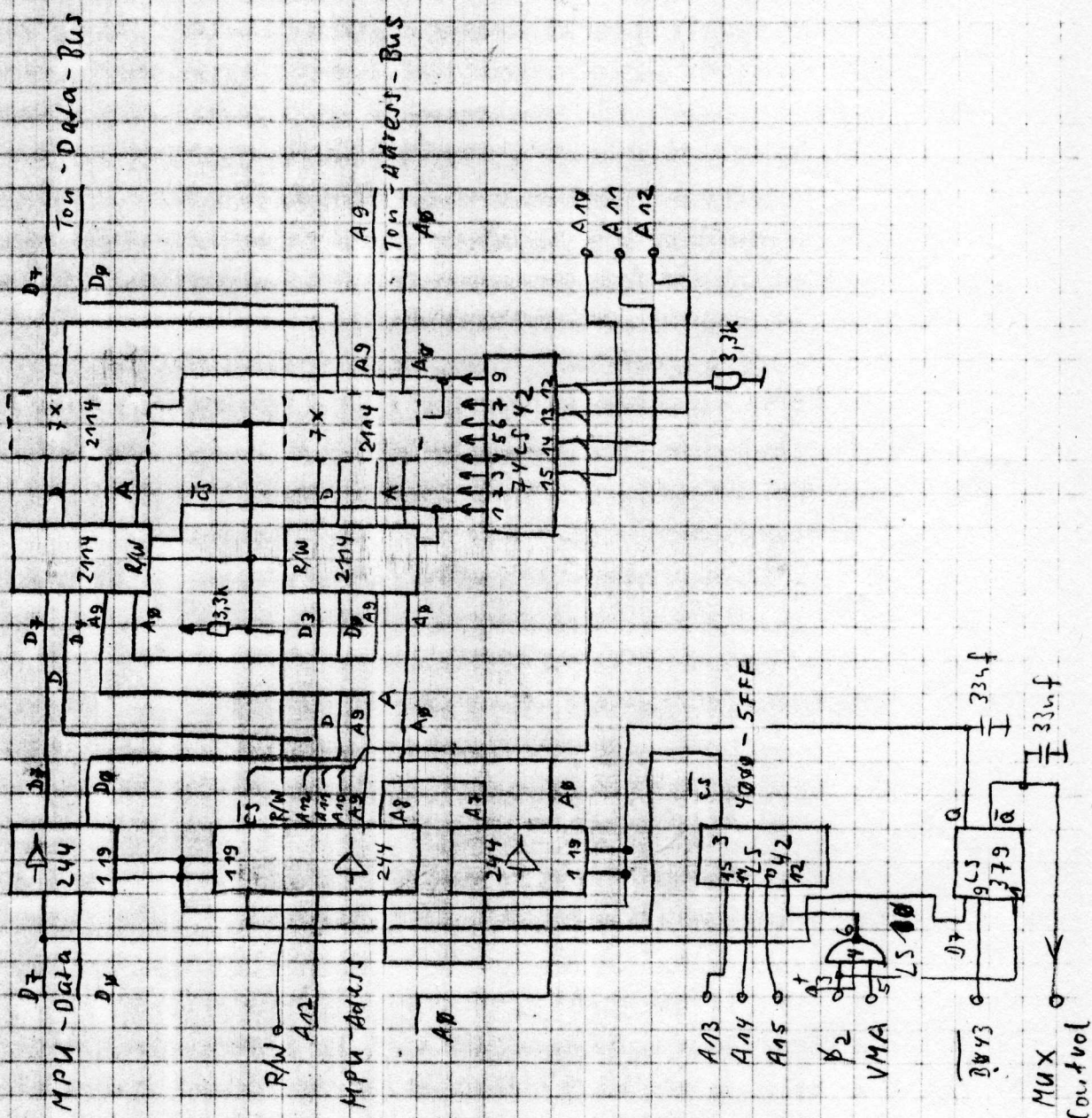
Per.	Adressen
Drucker	B480-3
DCR	B410-3
Terminhal	B498-1
Timer	B420-7
Termin. SW	B200

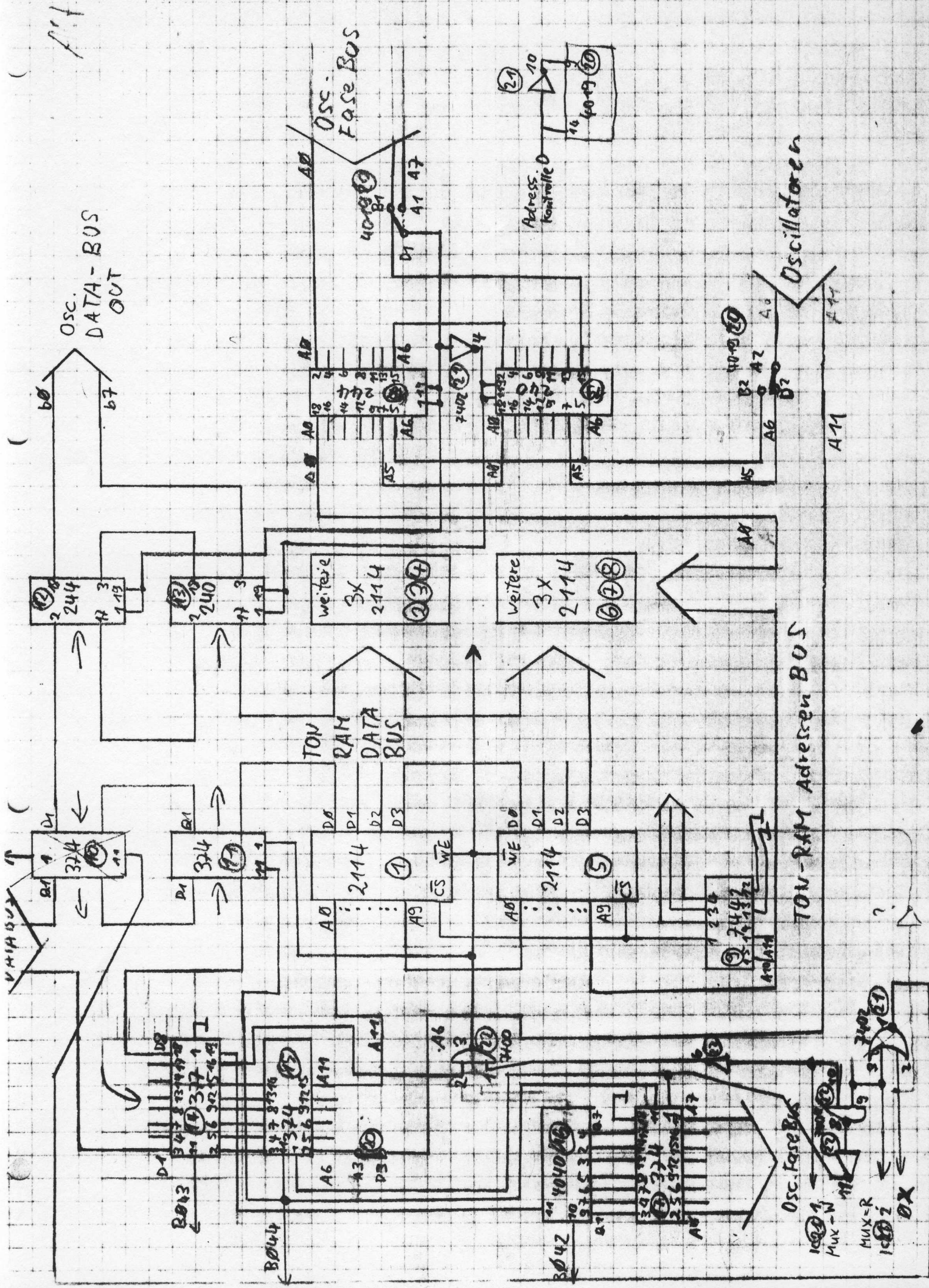


ACIA - 1100 H
300 h42

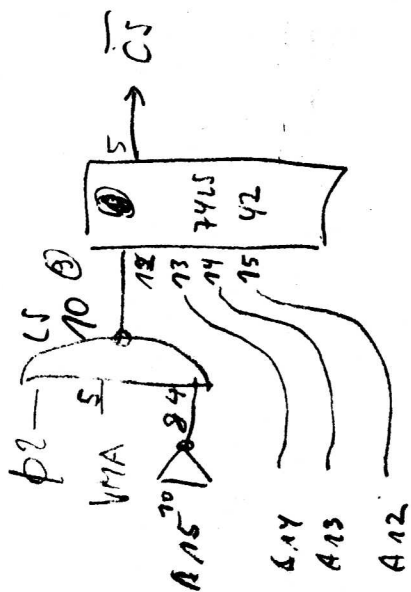
von Syst
2008

MPU - Stecker



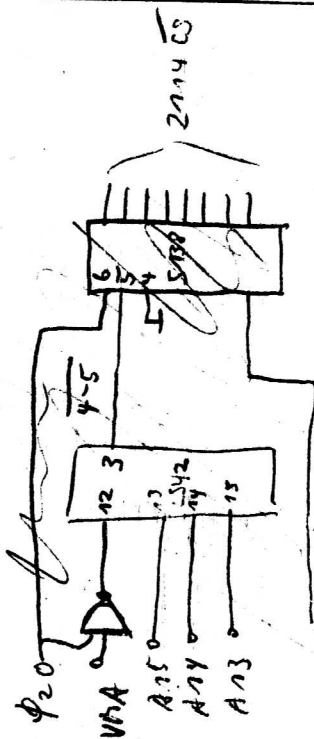


704 RAM
Basis

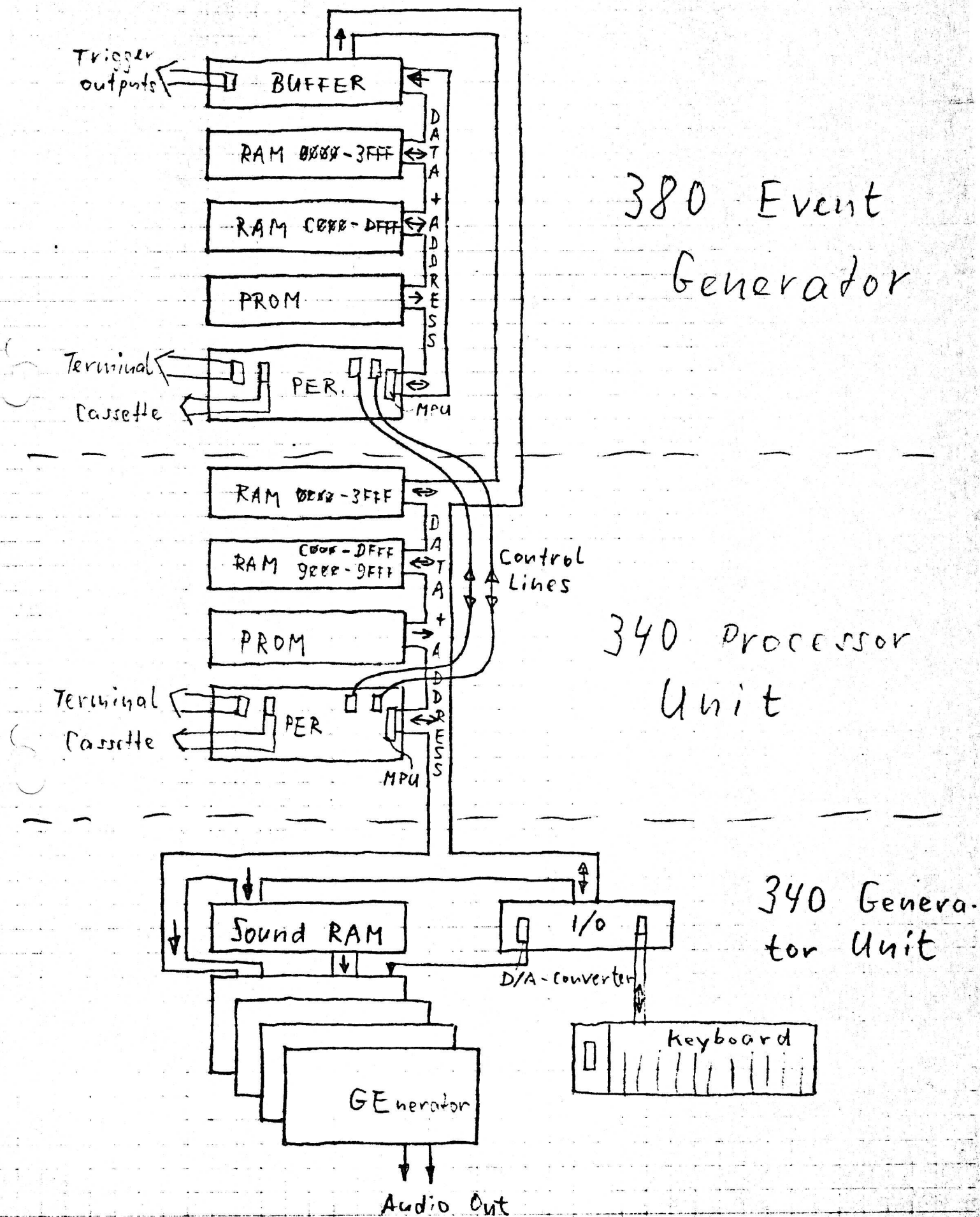


4000 - 5401
Advance

Basis: 4000 - 6000

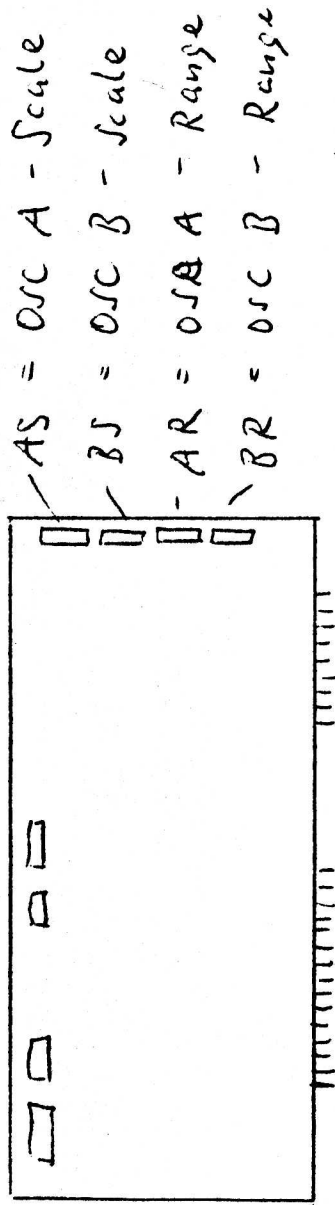


PPG-340/380 System



PPG 360 Oscillator Tuning

340 Generator Unit

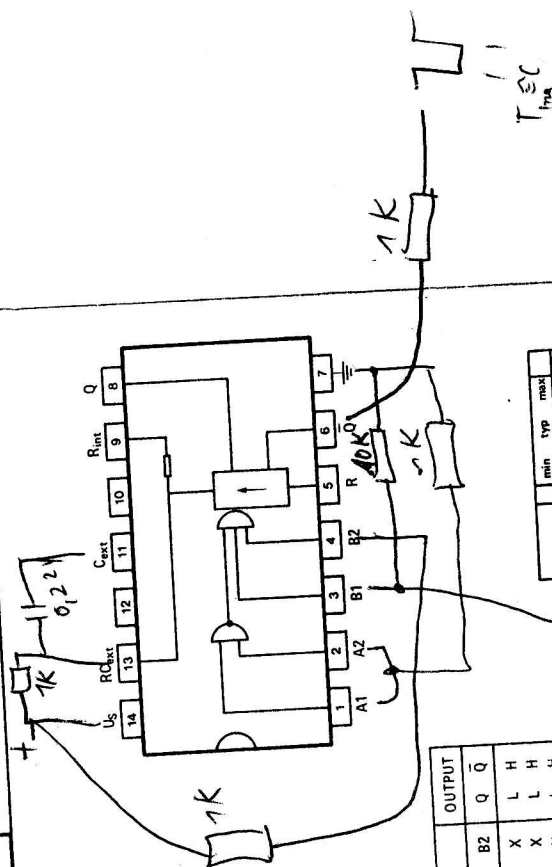


Each of the eight Oscillators have to be tuned as follows:

- 1, Press Note A[#] (B^b) and tune with trim potentiometer "RANGE"
 - 2, Press Note B (H) and tune with trim potentiometer "SCALE"
- repeat this

Pin	Fi L
A,B	1 4,5
R	2 9

Nachtriggerbares Monoflop
Retriggerable monostable multivibrator
Monoflop à déclenchement ultérieur
Monoflop a scatto addizionale



	min	typ	max
A → Q	↑	22	33
A → Q	↑	30	40
B → Q	↑	28	ns
B → Q	↑	27	36
R → Q	↑	18	27
R → Q	↑	30	40
t_Q		45	65

• Gültig für alle Typen
• Valido per tutti i tipi
• Valable pour tous les types
• Valid for all types

		INPUT					OUTPUT	
		R	A1	A2	B1	B2	Q	$\bar{Q}$
CLOCK	1	L	X	X	X	X	L	H
	2	X	H	X	X	L	L	H
	3	X	X	X	X	L	L	H
	4	X	X	X	X	L	L	H
	5	X	X	X	X	L	L	H
	6	X	X	X	X	L	L	H
	7	X	X	X	X	L	L	H
	8	X	X	X	X	L	L	H
	9	X	X	X	X	L	L	H
	10	X	X	X	X	L	L	H
	11	X	X	X	X	L	L	H
	12	X	X	X	X	L	L	H
	13	X	X	X	X	L	L	H
	14	X	X	X	X	L	L	H
	15	X	X	X	X	L	L	H
	16	X	X	X	X	L	L	H

01177H

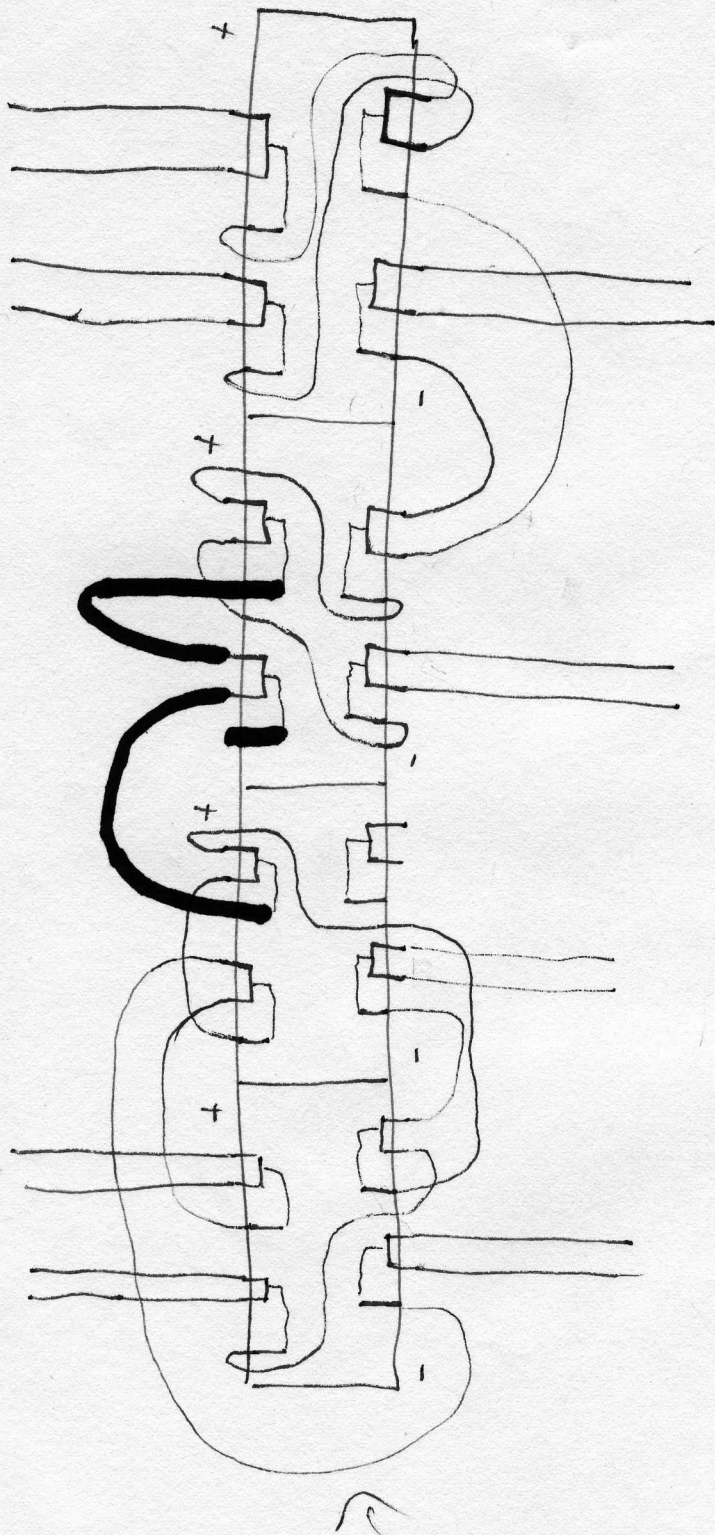


Fig Summe

LS 32

Fig Kosten

Masse / 380 cm

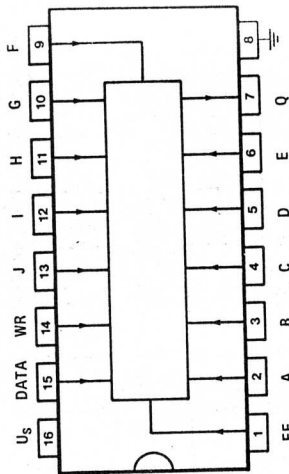
8.2.20 LS227

76	76	
77,60	77,60	
8,00	8,00	LS32=4X
25,60	25,60	
72,80	72,80	76-8
38,40	38,40	72,8-70
8,00	8,00	0
46,40	46,40	0,22V-162,50PF

Kosten Trig Voller Bauteile

1024x1-bit random access memory with tri-state outputs

1024x1-Bit Schreib-/Lesespeicher mit Tri-State Ausgängen
1024x1-bit random access memory with tri-state outputs
Mémoire d'inscription/lecture à 1024x1 bits avec sortie à trois états
Memoria d'iscrizione/lettura di 1024x1 bits con uscita a tre stati



Weitere Dateninformationen entnehmen Sie bitte unserer neuen
Microcomputer - Tabelle (ECA "µC1").

Please consult our new Microcomputer - Table for any further data information (ECA 'μC1').

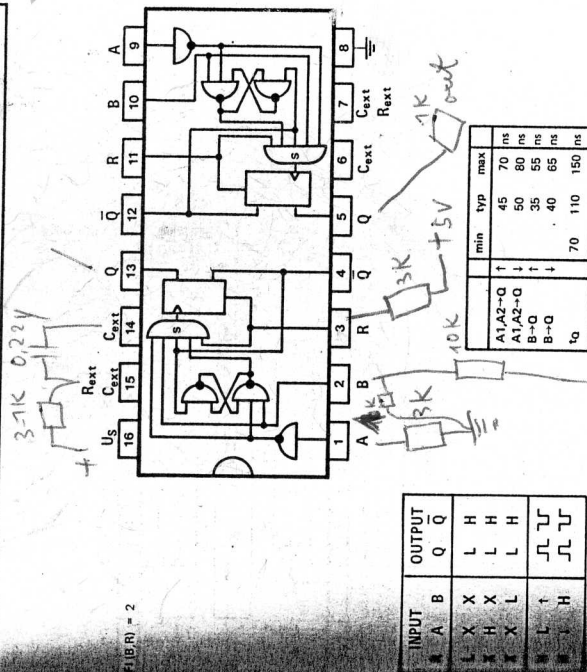
Nous vous prions de vouloir consulter notre nouvelle Table Micro-computer pour toute information ultérieure de données (ECA, $\mu C1$).

per ulteriori informazioni sui dati Vi preghiamo di consultare la nostra nuova Tabella Microcomputer (ECA µC1).

2-316

2 Monoflops mit Schmitt-Trigger Eingang
2 monostable multivibrators with Schmitt-trigger inputs
2 monoflops à entrée de déclencheur Schmitt
2 monoflops con ingresso trigger Schmitt

2 Monoflops mit Schmitt-Trigger Eingang
2 monostable multivibrators with Schmitt-trigger inputs
2 monoflops à entrée de déclencheur Schmitt
2 monoflops con ingresso trigger Schmitt



INPUT			OUTPUT
A	B	Q	$\bar{Q}$
L	X	L	H
X	X	L	H
X	H	L	H
X	L	L	H

*Valido per tutti i tipi
*Valable pour tous les types

84221	Type · Type · Type · Tipo		Hersteller Manufact. Fabricants Sect. Productori	B gPmax Sect. gP 3	F _{typ} A- $\bar{Q}$ B- $\bar{Q}$ A- $\bar{Q}$ B- $\bar{Q}$ F _{typ} (gPmax) gP (F _{typ}) 1	tp	tp	Bem. · Note	
	T _U = -25...85°C T _U = -55...125°C	T _U = -40...85°C T _U = -55...125°C							
84221	SN 54221 J	SN 54221 N	Hit	6a	180	50	40	35	
84221	SN 54221 J	SN 54221 N	Tix	6c	180	50	40	35	
84221	SN 54221 J	SN 54221 N	Tix	6a	180	50	40	35	
84221	SN 54221 J	SN 54221 N	Sie	6a	180	50	40	35	

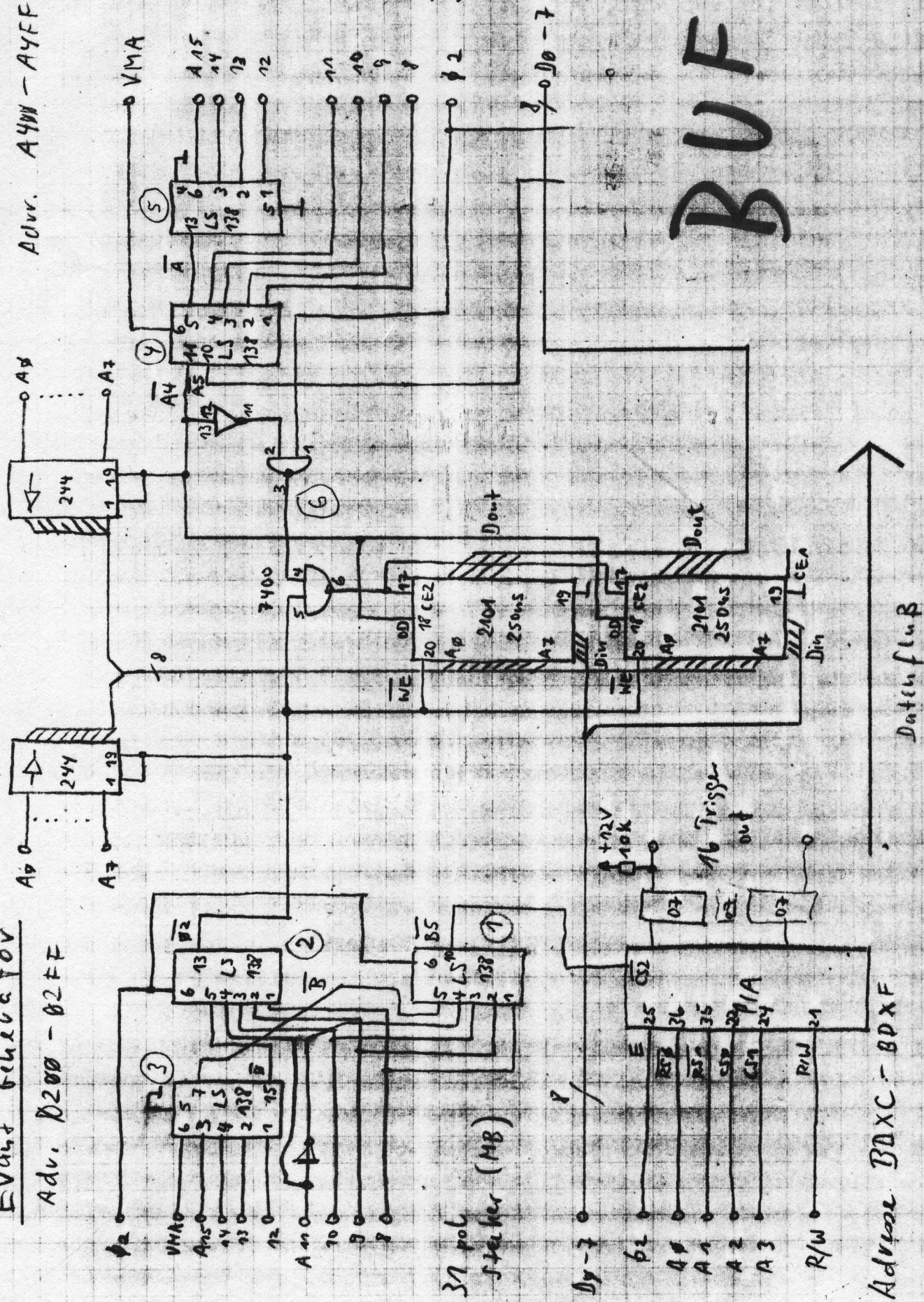
Datenbuffer - RAM

340 Wave Loup.

Adv. A400 - A4FF

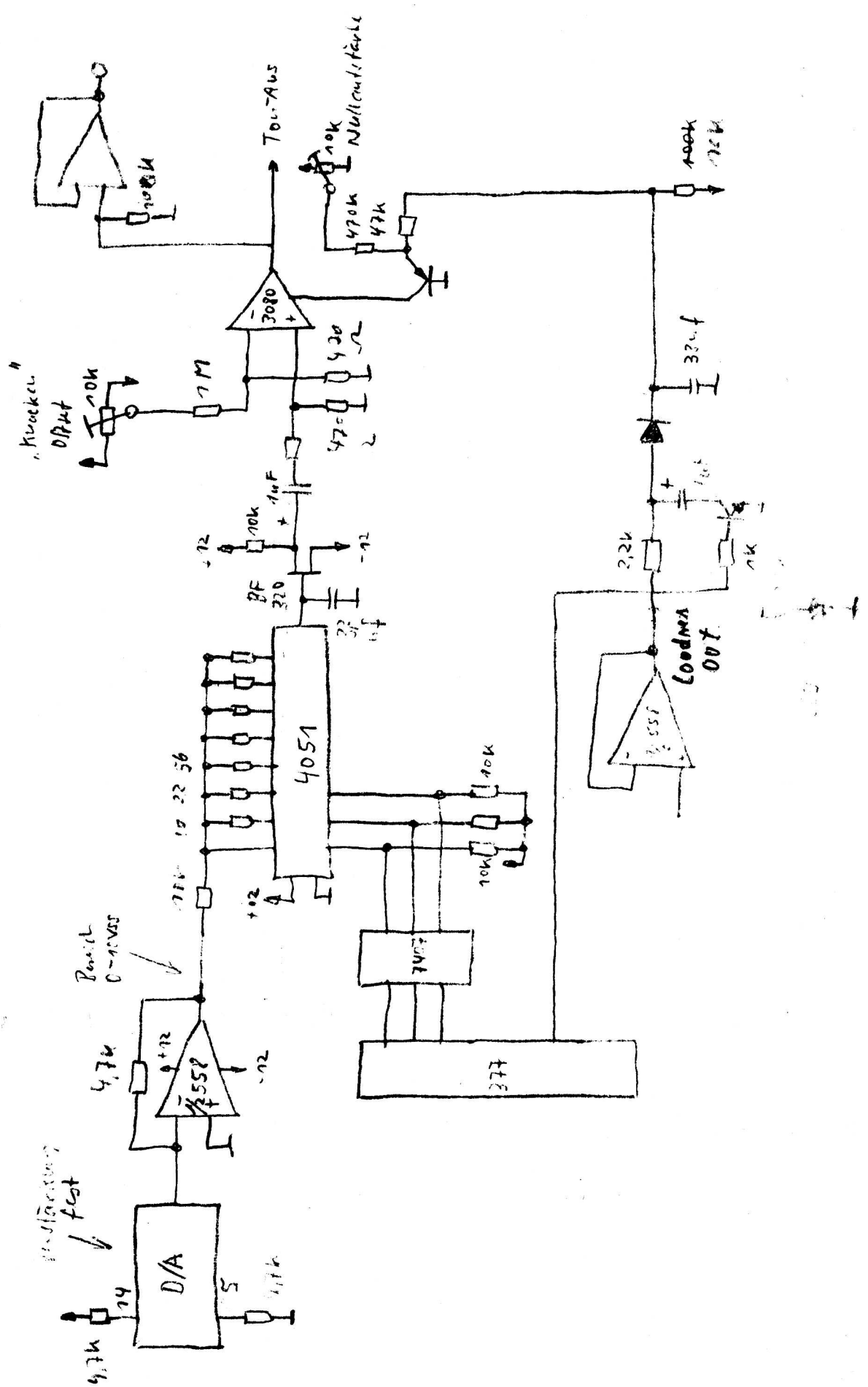
Event Generator

Adv. D200 - D2FF



Adresse BDXC - BDXF

Datenfließ



W
D

